

1 We claim:

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3 1. A method of demodulating multiple channels, comprising:

- 4 a) providing a first analog to digital converter having an analog input and a
5 digital output;
6 b) providing a first plurality of digital demodulators, each demodulator
7 having a programmable center frequency;
8 c) coupling a band of frequencies to the analog input of the converter, the
9 band including a second plurality of channels;
10 d) creating digitized samples of the band at the output of the first converter;
11 e) coupling the digitized samples to the plurality of demodulators; and
12 f) demodulating a first plurality of channels from the band of frequencies.

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14 2) The method of claim one, further including:

- 15 a) maintaining pre-computed sets of D.C. filter coefficients in non-volatile
16 storage, each set corresponding to one of multiple prototype low-pass
17 digital filters, each prototype filter having one of a predetermined set of
18 bandwidths;
19 b) selecting a first center frequency and first bandpass bandwidth for
20 provisioning a first one of the first plurality of demodulators;
21 c) retrieving the D.C. coefficients associated with the first bandwidth;
22 d) subjecting the retrieved D.C. coefficients to a band-pass transformation
23 corresponding to the first center frequency;
24 e) loading the transformed coefficients into coefficient latches in the first
25 demodulator.
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1 3. The method of claim 3, further including:

- 2 a) operating the first demodulator at the first desired center frequency;
- 3 b) subsequent to said operating, loading the coefficient latches in the first
- 4 demodulator with transformed coefficients corresponding to a second
- 5 desired center frequency; and
- 6 c) operating the first demodulator at the second desired center frequency.
- 7

8 4. The method of claim 3, further including:

- 9 a) selecting a second center frequency and second bandpass bandwidth for
- 10 provisioning a second one of the first plurality of demodulators, wherein
- 11 said first and second bandpass bandwidths are unequal;
- 12 b) retrieving the D.C. coefficients associated with the second bandwidth;
- 13 c) subjecting the retrieved D.C. coefficients to a band-pass transformation
- 14 corresponding to the second center frequency; and
- 15 d) loading the transformed coefficients into coefficient latches in the second
- 16 demodulator.
- 17

18 5. The method of claim 1, wherein the converter and the demodulators are within the

19 upstream section of a CMTS channel bank organized into upstream and

20 downstream channels.

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22 6. The method of claim 5, wherein the ratio of the number of upstream channels

23 demodulated by the CMTS channel bank to the number of upstream input

24 connectors of the CMTS channel bank is M.

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- 1 7. The method of claim 6, wherein M is 16.
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- 3 8. The method of claim 2, wherein the converter, the demodulators, and the non-
- 4 volatile storage, are implemented on a single integrated circuit.
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- 6 9. The method of claim 5, wherein the CMTS channel bank is organized using a
- 7 plurality of modules, each module having a third plurality of downstream
- 8 channels and and fourth plurality of upstream channels.
- 9
- 10 10. The method of claim 9, wherein the third plurality is 4 and the fourth plurality is
- 11 16.
- 12
- 13 11. The method of claim 9, wherein the channel bank has 8 modules.
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- 15 12. The method of claim 5, wherein the CMTS channel bank has 32 downstream
- 16 channels and 128 upstream channels.
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- 18 13. The method of claim 5, wherein the CMTS is DOCSIS compatible.
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- 20 14. The method of claim 5, wherein the upstream channels are in the 750-1000 MHz
- 21 portion of the spectrum.
- 22
- 23 15. The method of claim 14, wherein at least one frequency stacker is used to densely
- 24 pack each sub-band of the 750-1000 MHz spectrum portion.
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- 1 16. The method of claim 1, wherein each demodulator uses an FIR digital filter.
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- 3 17. The method of claim 16, wherein each FIR filter is an Optimum Equiripple
4 Linear-Phase filter.
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- 6 18. The method of claim 14, wherein the filter coefficients are designed using a
7 Chebyshev approximation.
8
- 9 19. The method of claim 18, wherein the Parks-McClellan Alternation theorem is
10 used in the approximation.
11
- 12 20. The method of claim 19, wherein the coefficients are computed using the Remez
13 exchange algorithm.
14
- 15 21. The method of claim 19, wherein the coefficients are computed using the Rabiner
16 exchange algorithm.
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- 18 22. The method of claim 2, wherein the number of coefficients for each filter is at
19 least 16.
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- 21 23. The method of claim 2, wherein the number of coefficients for each filter is at
22 most 24.
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